|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Op-code | RegDst | Jump | Branch | MemRead | MemtoReg | ALUop | MemWrite | ALUSrc | RegWrite |
| 000\_000 | 01 | 0 | 0 | 0 | 00 | 10 | 0 | 0 | 1 |
| 001\_000 | 0 | X | X | 0 | 00 | 10 | 0 | 1 | 1 |
| 100\_011 | 00 | 0 | 0 | 1 | 01 | 00 | 0 | 1 | 1 |
| 101\_011 | xx | 0 | 0 | 0 | Xx | 00 | 1 | 1 | 0 |
| 001\_100 | 0 | X | X | 0 | 00 | 10 | 0 | 1 | 1 |
| 000\_100 | xx | 0 | 1 | 0 | Xx | 01 | 0 | 0 | 0 |
| 000\_000 | xx | 1 | X | 0 | Xx | xx | 0 | X | 0 |
| 000\_001 | 10 | 1 | x | 0 | 10 | xx | 0 | X | 1 |
| 000\_000 |  |  |  |  |  |  |  |  |  |

1. R-type //Verified from the Reference.
2. Addi
3. Lw //Verified from the Reference.
4. Sw //Verified from the Reference.
5. Andi
6. Beq //Verified from the Reference.
7. J //Verified from the Reference.
8. Jal //Verified from the Reference.
9. Jr

Reference: <http://meseec.ce.rit.edu/eecc550-winter2005/550-chapter5-exercises.pdf>