|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Op-code | RegDst | Jump | Branch | MemRead | MemtoReg | ALUop | MemWrite | ALUSrc | RegWrite |
| 000\_000 | 1 | xx | xx | 0 | 0 | 10 | 0 | 0 | 1 |
| 001\_000 | 0 | xx | xx | 0 | 0 | 10 | 0 | 1 | 1 |
| 100\_011 | 0 | xx | xx | 1 | 1 | 00 | 0 | 1 | 1 |
| 101\_011 | 0 | xx | xx | 0 | 0 | 00 | 1 | 1 | 0 |
| 001\_100 | 0 | xx | xx | 0 | 0 | 10 | 0 | 1 | 1 |
| 000\_100 | 0 | 0 | 1 | xx | xx | 01 | xx | 0 | xx |
| 000\_000 | xx | 1 | xx | xx | xx | xx | xx | xx | xx |

1. R-type
2. Addi
3. Lw
4. Sw
5. Andi
6. Beq
7. JR